

ECE 261 Lecture Plan

Dates	Description	Text Reading
8/29	Course description & Background material	Chapter 1
8/31	Introduction To CMOS Circuits	Chapter 1
9/5, 9/7, 9/12	MOS Theory, fabrication and layout, design rules	Chapter 1-3
9/13 (wed) or 9/14 (thurs)	Lab Sessions (2.5 hours in DSIL lab)	
9/19, 9/21	Circuit Characterization and performance	Chapter 4
9/26, 9/28, 10/03	Static CMOS Combinational logic design	Chapter 6
10/05	Project Proposal Presentations	
10/10	Fall Break	
10/12	Dynamic CMOS Combinational logic design I	Chapter 6
10/17	Exam I (out of town)	
10/19	Dynamic CMOS Combinational logic design II	Chapter 6
10/24	Sequential Circuit Design	Chapter 7
10/26	Design Methods and Tools	Chapter 8
10/31, 11/2, 11/7	CMOS Circuit Testing	Chapter 9
11/9	Exam II	
11/14, 11/16	Subsystem Design: datapaths, adder design	Chapter 10
11/21	Multipliers, shifters, counters	Chapter 11
11/23	Thanksgiving Break	
11/28	Project Presentations (special evening sessions)	
11/30	Exam III	