

ECE 261 Lecture Plan for 2009

Dates	Description	Text Reading
8/25	Course description & Background material	Chapter 1
8/27	Introduction To CMOS Circuits	Chapter 1
9/1, 9/3	MOS Theory, fabrication and layout, design rules	Chapter 1-3
9/8	Circuit Characterization and performance (Part 1)	Chapter 4
9/10 (Thurs) or 9/11 (Fri)	Lab Sessions (2.5 hours in DSIL lab)	
9/15, 9/17	Circuit Characterization and performance (Part 2)	Chapter 4
9/22	Exam I	
9/24	Interconnects & Project Presentations	Chapter 4
9/29, 10/01	Static CMOS Combinational logic design & Project Presentations	Chapter 6
10/6	Fall Break (no class)	
10/8, 10/13	Dynamic CMOS Combinational logic design I & II	Chapter 6
10/15	Sequential Circuit Design I	Chapter 7
10/20	Exam II	
10/22	Sequential Circuit Design II	Chapter 7
10/27	Design Methods and Tools	Chapter 8
11/29	Circuit Pitfalls	Chapter 9
11/3	Project Proposal Presentations II	
11/5	Arithmetic Circuits I Subsystem Design, Adders	Chapter 10
11/10	CMOS Circuit Testing I, II (Guest Lecturer Dr. Chakrabarty)	Chapter 9
11/12	Arithmetic Circuits II multipliers, shifters, parity, Memory	Chapter 10
11/17	Exam III	
11/19	Final Project Presentations	
11/26 - 11/28	Thanksgiving Break (no classes)	
Final Exam period (12/3)	Final Project Grading and demonstration	