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## ECE 261 Lecture Plan for 2008

<b>Dates</b>	<b>Description</b>	<b>Text Reading</b>
8/27	Course description & Background material	Chapter 1
8/29	Introduction To CMOS Circuits	Chapter 1
9/3, 9/5	MOS Theory, fabrication and layout, design rules	Chapter 1-3
9/10	Circuit Characterization and performance (Part 1)	Chapter 4
9/11 (Thurs) or 9/12 (Fri)	Lab Sessions (2.5 hours in DSIL lab)	
9/17, 9/19	Circuit Characterization and performance (Part 2)	Chapter 4
9/24	Interconnects	Chapter 4
9/26, 10/01	Static CMOS Combinational logic design	Chapter 6
10/03	Project Proposal Presentations	
10/8	Dynamic CMOS Combinational logic design I	Chapter 6
10/10	Exam I	
10/15	Fall Break	
10/17	Dynamic CMOS Combinational logic design II	Chapter 6
10/22, 10/24	Sequential Circuit Design	Chapter 7
10/29	CMOS Circuit Testing I, II	Chapter 8
10/31	Exam II	Chapter 9
11/5	Circuit Pitfalls	
11/7	Project Proposal Presentations II	
11/12	Design Methods and Tools	Chapter 9
11/14	Arithmetic Circuits I Subsystem Design, Adders, multipliers, shifters, parity	Chapter 10
11/19	Class make up on 11/23 @ 3pm	
11/21	Exam III	Chapter 11
11/23 @ 3pm	Arithmetic Circuits II Multipliers, shifters, Memory	Chapter 10
11/26 - 11/28	Thanksgiving Break	
12/3	Project Presentations	(In class and special evening sessions)