FIGURE 1.

Mixed-Signal CAD Software Data Flow

- **DA: Design Architect**
  - Schematic and Symbol Entry/Printing

- **DVE: Viewpoint Editor**
  - Create viewpoint and ICtrace files

- **QSIM: Logic Simulator**
  - Logic Library
  - Schematic verification

- **HSPICE: Circuit Sim**
  - Active device and passive component simulator

- **Eldo rF: Circuit Sim/Viewer**
  - S Parameter, Device and passive component GHz simulator

- **EZWAVE: Waveform Viewer**
  - DC and Transient Simulation Viewer
  - AC analysis simulation Viewer

- **IC: Layout Editor**
  - Custom Circuit layout and verification

- **ICextract: Parasitic Extraction, XCalibre**
  - Device Parm Extraction: AD, AS, PD, PS, NRD, NRS
  - Interconnect Parasitic Extraction: Lumped or Distributive Capacitor and Resistive

- **Matlab**
  - Architecture and System Design/Modeling

- **Mosis Foundry Technology Files**
  - .5um, .18um Groundrules, Device Models
  - Process Data, Packaging, etc
  - Foundries include AMI and TSMC

- **EZWAVE & DRC check**
  - Circuit layout and schematic verification (ami.5um.rules)

- **IView**
  - Interconnect Parasitic Extraction: Lumped or Distributive Capacitor and Resistive

- **netlist**

- **ic**

- **Process Ground Rules from Mosis**

Shaded software tools are provided by Mentor Graphics Corporation.

The Hspice and Awaves tools are provided by Synopsis.

The Matlab and Matlab toolboxes are provided by Mathworks.