CS 250 PRACTICE Midterm Exam 1

This is a full length practice midterm exam. If you want to take it at exam pace, give yourself 75 minutes to take the entire test. Just like the real exam, each question has a point value. There are 75 points in the exam, so that you can pace yourself to average 1 point per minute (some parts will be faster, some slower).

Questions:

1. Numbers [10 points]
2. Binary Math [5 points]
3. UNIX Commands [5 points]
4. C Programming [15 points]
5. C Tracing [9 points]
6. ISA Concepts [6 points]
7. Asm Tracing [5 points]
8. Asm Programming [20 points]
Question 1: Numbers [10 pts]

1. Convert the decimal number -67 to 8-bit, signed 2’s complement binary:

2. Convert the 8-bit signed 2’s complement hex number 0x3F to decimal:

3. Convert the 16-bit signed 2’s complement binary number 1001 0001 1111 1010 to hex:
4. Convert -35.75 to its hexadecimal representation in IEEE floating point format:

5. Convert the hexadecimal IEEE format floating point number 0x40200000 to decimal:
Question 2: Binary Math [5 pts]

1. Add the 8-bit 2’s complement numbers 1110 1001 + 1100 0010. State the result of the addition (as a binary value), as well as whether overflow occurred if the number were treated as signed, and whether overflow occurred if the numbers were treated as unsigned. Show your work.

\[
\begin{array}{cccccccc}
1 & 1 & 1 & 0 & 1 & 0 & 0 & 1 \\
1 & 1 & 0 & 0 & 0 & 0 & 1 & 0 \\
\end{array}
\]

Signed Overflow:

Unsigned Overflow:

2. What is 0110 1101 \( \wedge \) 1100 1001?
3. Add the 8-bit 2’s complement numbers 0110 1111 + 0110 1010. State the result of the addition (as a binary value), as well as whether overflow occurred if the number were treated as signed, and whether overflow occurred if the numbers were treated as unsigned. Show you work.

```
0 1 1 0 1 1 1 1
0 1 1 0 1 0 1 0
```

Sum:

Signed Overflow:

Unsigned Overflow:

4. What is 1100 0011 >> 0000 0011 when the operands are treated as signed integers?

5. What is 1100 0011 >> 0000 0011 when the operands are treated as unsigned integers?
Question 3: UNIX Commands [5 pts]

Match each of the following tasks with the UNIX program most suited to that task:

1. Compile a C program
   - E gcc

2. Copy a file to a different computer
   - M scp

3. Edit a text file
   - C edit

4. View the contents of a file (not to edit it)
   - G less

5. Show what files are in the current directory
   - H ls
Question 4: C Programming [15 pts]

Given the following linked list node definition:

```c
struct ll_node {
    int data;
    struct ll_node * next;
};
typedef struct ll_node node;
```

Fill in the function below which finds the largest element in the list. If the list is empty, this function should return 0 (although, a non-empty list may contain only negative numbers, in which case, your code should function correctly).

```c
int findMax(node * head) {
```
Question 5: C Tracing [9 pts]
What is the output when the following C program is run?

```c
#include <stdio.h>

void f (int x, int * p) {
    *p = x;
    x = 33;
}

int main(void) {
    int a = 4;
    int b = 2;
    int * p = &a;
    *p = 55;
    printf("1: a = %d, b=%d\n", a, b);
    f(a,&b);
    printf("2: a = %d, b=%d\n", a, b);
    int ** q = & p;
    *q = & b;
    *p = 42;
    printf("3: a = %d, b=%d\n", a, b);
    return 0;
}
```

Question 6: ISA Concepts [6 pts]
Briefly answer each of the following:

1. ISA stands for “Instruction Set Architecture,” but what does it mean?

2. What is an advantage of a 3-operand ISA over a 2-operand ISA? (Hint: you might give an example assembly sequence)
**Question 7: Asm Tracing [5 pts]**

Trace (execute by hand) the following fragment of assembly. The starting register and memory state is given to you in the table below. Write the final register and memory state in the blank row of the table.

```
lw $t0, 0($sp)
lh $t1, 3
add $t0, $t0, $t1
sw $t0, 4($sp)
lw $v0, -8($fp)
```

<table>
<thead>
<tr>
<th>Registers</th>
<th>Memory</th>
</tr>
</thead>
<tbody>
<tr>
<td>t0</td>
<td>0x00</td>
</tr>
<tr>
<td>t1</td>
<td>0x00</td>
</tr>
<tr>
<td>sp</td>
<td>0x1D0</td>
</tr>
<tr>
<td>fp</td>
<td>0x1DC</td>
</tr>
<tr>
<td>v0</td>
<td>0x99</td>
</tr>
<tr>
<td>0x1D0</td>
<td>0x11</td>
</tr>
<tr>
<td>0x1D4</td>
<td>0x22</td>
</tr>
<tr>
<td>0x1D8</td>
<td>0x33</td>
</tr>
<tr>
<td>0x1DC</td>
<td>0x44</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Initial</th>
</tr>
</thead>
<tbody>
<tr>
<td>Final</td>
</tr>
</tbody>
</table>
Question 8: Asm Programming [20 pts]
Write MIPS assembly for the following C function.

```c
int countMatching(int * ptr, int n) {
    int count = 0;
    while (n > 0) {
        int x = testFn(*ptr);
        if (x != 0) {
            count++;
        }
        ptr++;
        n--;
    }
    return count;
}
```

Answer on next page
.globl countMatching
.ent countMatching
.text
countMatching:

.end countMatching